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**Complete if Known**

Application Number	10/654,318
Filing Date	September 3, 2003
First Named Inventor	Javendar Rajagopalan
Art Unit	2817
Examiner Name	(Not Yet Assigned)
Attorney Docket Number	P05655

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**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	AA	MARGARIT, Mihai A., et al, "A Low-Noise, Low-Power VCO With Automatic Amplitude Control for Wireless Applications," IEEE Journal of Solid-State Circuits, Vol. 34, No. 6, June 1999, pp. 761-771.	
	AB	ROGERS, John W.M., et al., "A Study of Digital and Analog Automatic-Amplitude Control Circuitry for Voltage-Controlled Oscillators," IEEE Journal of Solid-State Circuits, Vol. 38, No. 2, February 2003, pp. 352-356.	

Examiner Signature		Date Considered	2/2/5
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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